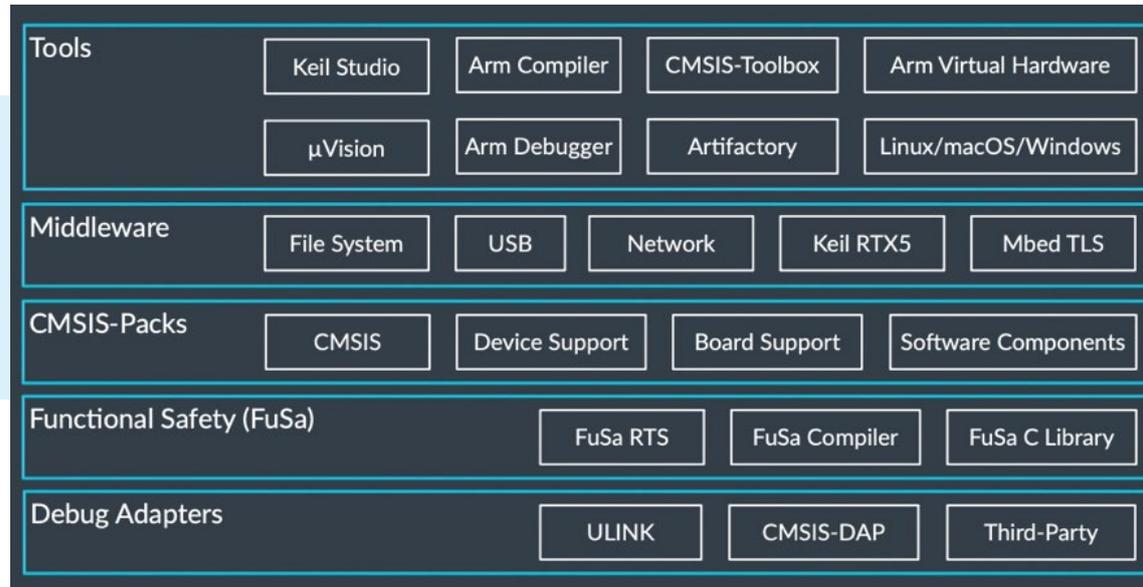


YOUR INNOVATIONS, OUR SOLUTIONS.

Arm Keil MDK v6

Next-Generation Embedded Development for Cortex-M & Ethos-U

- + Modern
- + Cross-Platform
- + Virtual Hardware
- + Integrated Functional Safety



KEY BENEFITS

Keil Studio	Powerful VS Code-based IDE (Windows, Linux, macOS) Native support for Cortex-M55, M85, Ethos-U (edge ML, high-performance DSP)
Arm Virtual Hardware (AVH)	Develop, test & debug without physical hardware
Modern CMSIS workflows	CMSIS-Toolbox, csolution projects, reproducible builds
Comprehensive middleware (now free)	RTOS, USB, File System, IPv4/IPv6 Networking
CI/CD & DevOps ready	GitHub integration, scalable automation & test pipelines
Per-user licensing	Ideal for teams, remote work & multi-OS flexibility
Legacy	µVision IDE included for smooth migration
Functional Safety	Certified Arm Compiler, RTX RTOS and Libraries

BUILD FASTER. TEST EARLIER. CERTIFY WITH CONFIDENCE.

Arm Keil MDK v6 – The Industry Standard for Cortex-M Embedded & Safety-Critical Applications

+ www.keil.arm.com

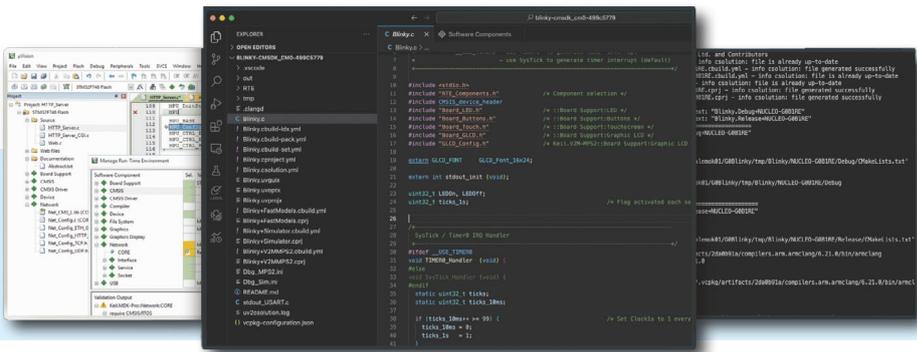
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Main Technical Features of Keil MDK v6

Keil MDK v6 combines modern development environments with established toolchains, CMSIS-based flows for maximum software reuse, and native support for functional safety in Arm Cortex-M (and Ethos-U) applications.

Development Environments

Keil Studio (VS Code-based) Extensions for CMSIS Solution (csolution projects), Arm CMSIS Debugger (GDB/MI), register viewer (CMSIS-SVD), serial/TCP terminal, memory analyzer, native GIT support

µVision Debugger with instruction-level trace, RTOS kernel awareness, Event Recorder for dynamic analysis

Toolchain and Compilation

Arm Compiler for Embedded Optimizations for Cortex-M (high code density, low cycle consumption)

Multi-toolchain support Arm GNU Toolchain, LLVM Embedded Toolchain

CMSIS Packs

Royalty-free Libraries RTX RTOS (CMSIS-RTOS v2 compliant)
 DSP/NN libraries
 Middleware Libraries
 + TCP/IP stack (IPv4/IPv6 dual-stack) + USB Host/Device + File System
 + IoT protocols + Graphics

Debug

Debug & Trace Support for ULINK2, ST-Link, JLink, ULINKplus (power profiling)
 Coverage analysis, execution profiling, code trace for optimization and verification

DevOps/MLOps Integration

CMSIS-Toolbox For reproducible CLI builds

AVH Precise Cortex-M and Corstone subsystems simulation (Fixed Virtual Platforms + Fast Models) automated testing in CI/CD pipelines without physical hardware

Functional Safety

Arm Compiler for Embedded FuSa Qualified version with validation reports for safety toolchain

FuSa RTS Certified runtime including
 + RTX RTOS (CMSIS-RTOS v2 compliant) + Safety-qualified C Library
 + CMSIS-Core for core/peripheral access + Event Recorder for logging safety-relevant events

Certifications

Pre-qualified (in combination with Arm Compiler FuSa) for
 + IEC 61508 SIL 3 + ISO 26262 ASIL D
 + IEC 62304 Class C + EN 50128 SIL 4

Benefits Reduces toolchain qualification effort
 Provides TÜV reports, documentation, source code for audits
 Native integration with debugger for tracing safety-critical paths

Explore CMSIS examples, AVH models, and FuSa RTS
 Complete documentation and technical resources
 + developer.arm.com/documentation